

JC972 U.S. PRO
 09/779374
 02/04/81

ISSUE CLASSIFICATION	
Class	Subclass
100	100
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PATENT NUMBER

U.S. UTILITY Patent Application

12W	O.I.P.E.	PATENT DATE
SCANNED	220 Q.A.	1A

APPLICATION NO. 09/779374	CONT/PRIOR D	CLASS 438	SUBCLASS 99	ART UNIT 28	EXAMINER SMITH Ramsley
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TITLE	APPLICANTS
1. Chairman	1. Mr. J. H. ...
2. Vice Chairman	2. Mr. J. H. ...
3. Secretary	3. Mr. J. H. ...
4. Treasurer	4. Mr. J. H. ...
5. Member	5. Mr. J. H. ...
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95. Member	95. Mr. J.

Vladimir Mancevski

System and method for fabricating logic devices comprising carbon nanotube transistors

PTO-2040
12/89

ISSUING CLASSIFICATION															
ORIGINAL				CROSS REFERENCE(S)											
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)										
INTERNATIONAL CLASSIFICATION															

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	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)		ISSUE FEE	
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